1   30. An encryption circuit according to claim 15 comprising a serial link (SL)

2   connected to input basic keys through a secure path independent of the dedicated PCI

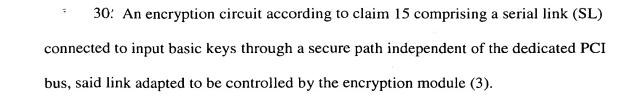3   bus, said link adapted to be controlled by the encryption module (3).

1   31. An encryption circuit according to claim 30, characterized in that the

2   serial link (SL) allows downloading of proprietary algorithms into the first encryption

3   sub-module (3_1).

1   32. An encryption circuit as set forth in claim 15 further including a card

2   supporting the circuit.

1   33. An encryption circuit as set forth in claim 18 further including a card

2   supporting the circuit.

1   34. An encryption circuit as set forth in claim 21 further including a card

2   supporting the circuit

IN THE ABSTRACT:

Delete the present Abstract in its entirety and replace with the one attached

hereto as Attachment A.

REMARKS

This Preliminary Amendment is made to eliminate informalities in the

specification, claims and abstract resulting from a literal translation of the French text,

to eliminate the use of multiple dependent claims, and to insert headings to conform

the application to U.S. practice.